

**IN THE CLAIMS:**

1. (currently amended) A power selection system for use with a reconfigurable circuit, comprising:

a monitoring circuit configured to monitor an operating characteristic associated with at least one node located within said reconfigurable circuit; and

a mode selection circuit coupled to said monitoring circuit and configured to select one of:

a normal power mode when said operating characteristic falls within a predetermined operating range of said reconfigurable circuit, and

an alternative power mode when said operating characteristic falls outside of said predetermined operating range of said reconfigurable circuit, said alternative power mode obtained by adapting said reconfigurable circuit.

2. (original) The power selection system as recited in Claim 1 wherein said monitoring circuit comprises a switching counter configured to monitor a number of switching transitions associated with said at least one node located within said reconfigurable circuit.

3. (original) The power selection system as recited in Claim 2 wherein said predetermined operating range comprises a threshold number of switching transitions.

4. (original) The power selection system as recited in Claim 3 wherein said mode selection circuit is configured to select one of:

said normal power mode when said number of switching transitions is less than or equal to said threshold number of switching transitions, and

said alternative power mode when said number of switching transitions is greater than said threshold number of switching transitions.

5. (original) The power selection system as recited in Claim 1 further comprising a timing counter configured to track a period of operation of said reconfigurable circuit.

6. (original) The power selection system as recited in Claim 1 wherein said alternative power mode is a low power mode.

7. (original) The power selection system as recited in Claim 1 wherein said reconfigurable circuit comprises a Pseudo Random Binary Sequence (PRBS) generator.

8. (currently amended) A method of operating a reconfigurable circuit, comprising:  
monitoring an operating characteristic associated with at least one node located within said reconfigurable circuit; and

selecting one of:

a normal power mode when said operating characteristic falls within a predetermined operating range of said reconfigurable circuit, and

an alternative power mode when said operating characteristic falls outside of said predetermined operating range of said reconfigurable circuit, said alternative power mode obtained by adapting said reconfigurable circuit.

9. (original) The method as recited in Claim 8 wherein said monitoring comprises monitoring a number of switching transitions associated with said at least one node located within said reconfigurable circuit.

10. (original) The method as recited in Claim 9 wherein said predetermined operating range comprises a threshold number of switching transitions.

11. (original) The method as recited in Claim 10 wherein said selecting comprises selecting one of:

said normal power mode when said number of switching transitions is less than or equal to said threshold number of switching transitions, and

said alternative power mode when said number of switching transitions is greater than said threshold number of switching transitions.

12. (original) The method as recited in Claim 8 further comprising tracking a period of operation of said reconfigurable circuit.

13. (original) The method as recited in Claim 8 wherein said reconfigurable circuit comprises a Pseudo Random Binary Sequence (PRBS) generator.

14. (currently amended) A reconfigurable circuit, comprising:

a monitored sub-circuit, including:

a delay element, associated with a node of said reconfigurable circuit, having a switch;

a multiplier interposed between said node and an output of said reconfigurable circuit; and

a power selection system, including:

a monitoring circuit that monitors an operating characteristic associated with said node, and

a mode selection circuit, coupled to said monitoring circuit, that selects one of:

a normal power mode when said operating characteristic falls within a predetermined operating range of said reconfigurable circuit, and

an alternative power mode when said operating characteristic falls outside of said predetermined operating range of said reconfigurable circuit, said alternative power mode obtained by adapting said monitored sub-circuit.

15. (original) The reconfigurable circuit as recited in Claim 14 wherein said monitored sub-circuit comprises a plurality of delay elements, associated with a respective node of said reconfigurable circuit, having a corresponding switch and a plurality of multipliers interposed between said one of said nodes and said output of said reconfigurable circuit, said monitoring circuit monitoring an operating characteristic associated with at least one of said nodes.

16. (original) The reconfigurable circuit as recited in Claim 14 wherein said monitoring circuit comprises a switching counter that monitors a number of switching transitions associated with said switch associated with said node.

17. (original) The reconfigurable circuit as recited in Claim 16 wherein said predetermined operating range comprises a threshold number of switching transitions.

18. (original) The reconfigurable circuit as recited in Claim 17 wherein said mode selection circuit selects one of:

said normal power mode when said number of switching transitions is less than or equal to said threshold number of switching transitions, and

said alternative power mode when said number of switching transitions is greater than said threshold number of switching transitions.

19. (original) The reconfigurable circuit as recited in Claim 14 wherein said power selection system further comprises a timing counter that tracks a period of operation of said monitored sub-circuit.

20. (original) The reconfigurable circuit as recited in Claim 14 wherein said monitored sub-circuit is selected from the group consisting of:

a Pseudo Random Binary Sequence (PRBS) generator, and  
a filter circuit.